

COURSE DESCRIPTION

CHJ950 – PSR2/FUPLA2 Programming

Course goal

The goal of this course is to teach the student how to use the FUPLA2 programming tool with the help of table models with PSR hardware. Most of the time the student will work on his own by programming prepared projects.

Main learning objectives

The participants will be able to:

- Address the important device types
- Write application programs with FUPLA2
- Use the FUPLA2 debugger
- Modify existing FUPLA projects

Participant profile

This training is targeted to project design and commissioning engineers as well as testing engineers, who use a standardized PSR system.

Prerequisites

Knowledge in electronics and digital technique as well as personal computer knowledge is required.

Topics

- Introduction to PSR2 technology
- Functions and setting of the PSR2 devices, such as
 - Processing unit
 - I/O modules
 - Gate control unit
 - ARCnet devices

- Operating program language FUPLA2

- PTS Shell
- Editor
- Configuration manager
- Code generation
- Debugger, diagnosis
- PSRView application
- Function block library

- Programming aspects

- How to set up a project
- Program structure
- Data formats
- Memory mapping
- Macro technique

Course type and methods

- Lectures for introduction
- Hands-on training

Duration

The duration is 5 days.

Remarks

Custom-tailored and on-site training courses are offered on request.

Course map

	DAY 1	DAY 2	DAY 3	DAY 4	DAY 5
Topics	Welcome, personnel introduction	Review day 1 Setting up a project	Review day 2 Capabilities of PSR	Review day 3 Mailboxlong physical memory	Review day 4 Data transmission via Fieldbus
	Course introduction	Exercises	Memory range of BuslongIO	Comparison of BuslongIO and BusshortIO	ARCnet
	Terms used	Steps in the project generation	Devise assignment to BuslongIO	Knowledge of devices	AF C094 panel
	Structure of documentation	Exercises	Use of the BuslongIO	Macro technique PSRView	Programming exercises using ARCnet
	PSR2 technology		Exercise	Parameter adjustment	Questions and answers
	The devices			Exercises	Evaluation
	Mechanical construction				Course close
	Signal transition				
	Programming tool				
Time	9:00 am – 5:00 pm	9:00 am – 5:00 pm	9:00 am – 5:00 pm	9:00 am – 5:00 pm	9:00 am – 5:00 pm

Typical course layout (time or sequence may change)